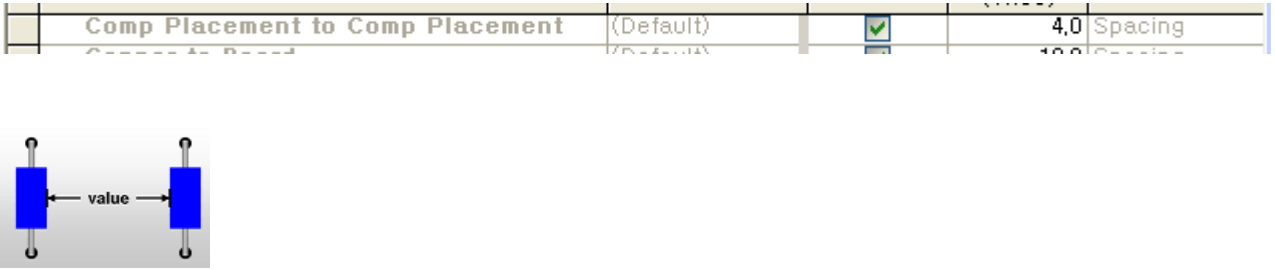
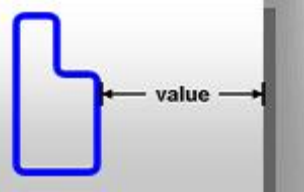

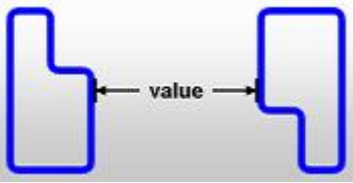
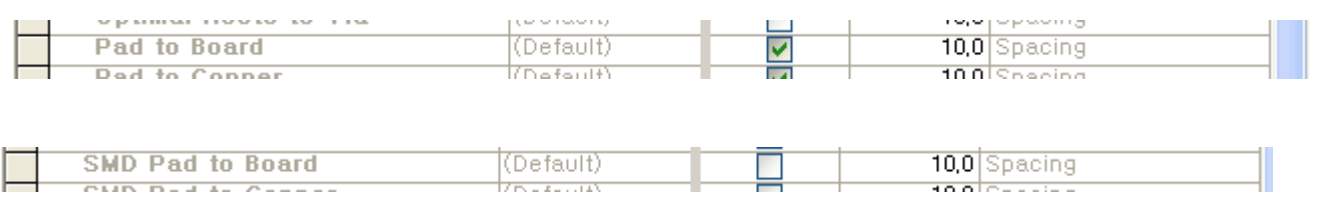
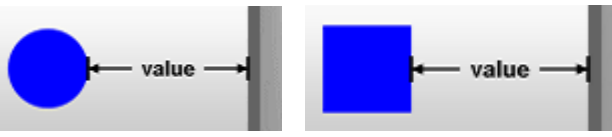

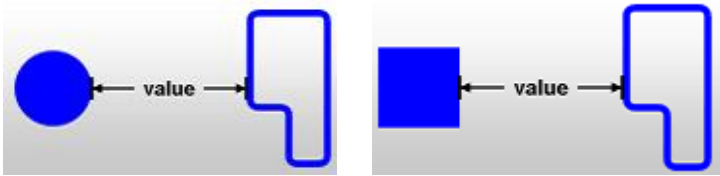


캐드스타 설계규정 지정과 Error Code와의 관계 - Spacing



1. Spacing

Error Code	Error Code 상세 설명	CADSTAR Assignment Setting 값
AA	Comp Placement to Comp Placement	
CB BT BC	Copper to Board Outline Text to Board Outline Comp Copper to Board Outline	 <p data-bbox="1099 1118 1951 1193">Text와 Comp Copper는 Design Rule Check 시에는 Copper로 취급하여 Copper to Board 설정 값을 이용한다.</p>


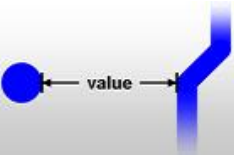
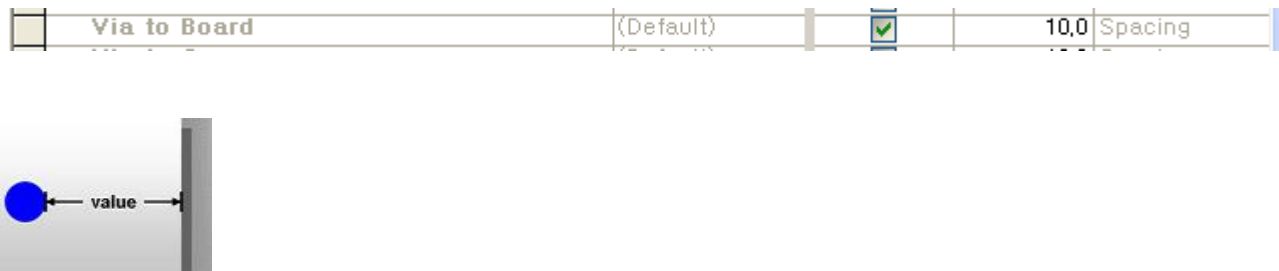
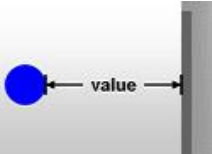

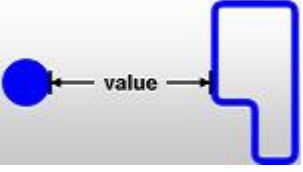
<p>CC XX XO XC CO CM</p>	<p>Copper to Copper Text to Text Text to Copper Text to Comp Copper Comp Copper to Copper Comp Copper to Comp Copper</p>	  <p>Text와 Comp Copper는 Design Rule Check 시에는 Copper로 취급하여 Copper to Copper 설정 값을 이용한다.</p>
<p>PB</p>	<p>Pad to Board Outline</p>	 


<p>PC XP CP</p>	<p>Pad to Copper Text to Pad Comp Copper to Pad</p>	 <p>Design Rule Check에서는 Through Hole Pad와 SMD Pad를 별도로 Check 할 수 있다. SMD Pad to Board Defind 항목을 Check 하면 Throught Hole Pad와 SMD Pad를 각각 다른 Setting 값을 지정하여 Check 할 수 있고 SMD Pad to Board Defind 항목을 Check 하지않으면 Throught Hole pad와 SMD Pad를 같이 취급하여 Pad to Copper 설정 값을 이용한다.</p>  <p>Text와 Comp Copper는 Design Rule Check 시에는 Copper로 취급하여 Copper to Pad 나 Copper to SMD Pad 설정 값을 이용한다.</p>
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PP	Pad to Pad	<table border="1"> <tr> <td>Pad to Pad</td> <td>(Default)</td> <td><input checked="" type="checkbox"/></td> <td>10,0</td> <td>Spacing</td> </tr> <tr> <td>Pad to SMD Pad</td> <td>(Default)</td> <td><input type="checkbox"/></td> <td>10,0</td> <td>Spacing</td> </tr> </table>	Pad to Pad	(Default)	<input checked="" type="checkbox"/>	10,0	Spacing	Pad to SMD Pad	(Default)	<input type="checkbox"/>	10,0	Spacing
Pad to Pad	(Default)	<input checked="" type="checkbox"/>	10,0	Spacing								
Pad to SMD Pad	(Default)	<input type="checkbox"/>	10,0	Spacing								
		<table border="1"> <tr> <td>SMD Pad to SMD Pad</td> <td>(Default)</td> <td><input type="checkbox"/></td> <td>10,0</td> <td>Spacing</td> </tr> </table>	SMD Pad to SMD Pad	(Default)	<input type="checkbox"/>	10,0	Spacing					
SMD Pad to SMD Pad	(Default)	<input type="checkbox"/>	10,0	Spacing								
		<p>Design Rule Check에서는 Through Hole Pad와 SMD Pad를 별도로 Check 할 수 있다.</p> <p>SMD Pad to SMD Pad Defind 항목을 Check 하면 Through Hole Pad와 SMD Pad를 각각 다른 Setting 값을 지정하여 Check 할 수 있고 SMD Pad to SMD Pad Defind 항목을 Check 하지않으면 Through Hole pad와 SMD Pad를 같이 취급하여 Pad to Pad 설정 값을 이용한다.</p>										
VP	Via to Pad	<table border="1"> <tr> <td>Pad to Via</td> <td>(Default)</td> <td><input checked="" type="checkbox"/></td> <td>10,0</td> <td>Spacing</td> </tr> <tr> <td>Via to SMD Pad</td> <td>(Default)</td> <td><input type="checkbox"/></td> <td>10,0</td> <td>Spacing</td> </tr> </table>	Pad to Via	(Default)	<input checked="" type="checkbox"/>	10,0	Spacing	Via to SMD Pad	(Default)	<input type="checkbox"/>	10,0	Spacing
Pad to Via	(Default)	<input checked="" type="checkbox"/>	10,0	Spacing								
Via to SMD Pad	(Default)	<input type="checkbox"/>	10,0	Spacing								
		<p>Design Rule Check에서는 Through Hole Pad와 SMD Pad를 별도로 Check 할 수 있다.</p> <p>Via to SMD Pad Defind 항목을 Check 하면 Through Hole Pad와 SMD Pad를 각각 다른 Setting 값을 지정하여 Check 할 수 있고 Via to SMD Pad Defind 항목을 Check 하지않으면 Through Hole pad와 SMD Pad를 같이 취급하여 Pad to Via 설정 값을 이용한다.</p>										

TB	Route to Board Outline	 <p>The diagram shows a blue route connecting to a grey board outline. A double-headed arrow labeled 'value' indicates the distance between the route and the board outline.</p>	<p>Teardrop은 Design Rule Check 시에는 Route로 취급하여 Route to Board 설정 값을 이용한다.</p>
BD	Teardrop to Board Outline		
TC	Route to Copper	 <p>The diagram shows a blue route connecting to a blue copper shape. A double-headed arrow labeled 'value' indicates the distance between the route and the copper shape.</p>	<p>Teardrop은 Design Rule Check 시에는 Route로 취급하여 Route to Copper 설정 값을 이용한다. Text와 Comp Copper는 Design Rule Check 시에는 Copper로 취급하여 Route to Copper 설정 값을 이용한다.</p>
EO	Teardrop to Copper		
ET	Teardrop to Text		
XR	Text to Route		
EC	Teardrop to Comp Copper		
CR	Comp Copper to Route		

<p>TP Route to Pad EP Teardrop to Pad</p>		 <p>Design Rule Check에서는 Through Hole Pad와 SMD Pad를 별도로 Check 할 수 있다. Route to SMD Pad Defind 항목을 Check 하면 Throught Hole Pad와 SMD Pad를 각각 다른 Setting 값을 지정하여 Check 할 수 있고 Route to SMD Pad Defind 항목을 Check 하지않으면 Throught Hole pad와 SMD Pad를 같이 취급하여 Pad to Via 설정 값을 이용한다.</p>   <p>Teardrop은 Design Rule Check 시에는 Route로 취급하여 Route to Pad 설정 값을 이용한다.</p>
<p>TT Route to Route ER Teardrop to Route EE Teardrop to Teardrop</p>		  <p>Teardrop은 Design Rule Check 시에는 Route로 취급하여 Route to Pad 설정 값을 이용한다.</p>

<p>TV EV</p>	<p>Route to Via Teardrop to Via</p>	 <p>Route to Via (Default) [checked] 10.0 Spacing</p> <p>Teardrop to Via (Default) [checked] 10.0 Spacing</p>  <p>Teardrop은 Design Rule Check 시에는 Route로 취급하여 Route to Pad 설정 값을 이용한다.</p>
<p>VB</p>	<p>Via to Board Outline</p>	 <p>Via to Board (Default) [checked] 10.0 Spacing</p> 
<p>VC XV CV</p>	<p>Via to Copper Text to Via Comp Copper to Via</p>	 <p>Via to Copper (Default) [checked] 10.0 Spacing</p>  <p>Text와 Comp Copper는 Design Rule Check 시에는 Copper로 취급하여 Copper to Board 설정 값을 이용한다.</p>

W	Via to Via	
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이 기술자료 내용에 관한 문의사항이 있으시면 아래로 연락 주시기 바랍니다.

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